FORM PTO-1449	ATTY. DOCKET NO. 0056.10US	APPLICATION NO. 09/223,046
√ FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	APPLICANT Hsu et al.	
	FILING DATE December 30, 1998	GROUP ~~2783~~ 2154

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
✎	AK3	5,793,661	08/1998	Dulong et al.	364	750.5	

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
✎	AL1	WO 97/07450	02/1997	~~PCT~~ WO	~~G06F~~	7/00	Yes No
✎	AM1	WO 97/08608	03/1997	~~PCT~~ WO	~~G06F~~	7/00	Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

✎	AQ	9	*i860™ Microprocessor Family Programmer's Reference Manual*, Intel Corporation, 1992, 79 pages, (Bates Numbers L09361-L09439).
✎	AR	9	Lee, Ruby, "Accelerating Multimedia with Enhanced Microprocessors," *IEEE Micro*, IEEE, April, 1995, pp. 22-32, (Bates Numbers L08566-L08576).
✎	AS	9	*DSP56000 24-Bit Digital Signal Processor Family Manual*, Motorola, Inc., 1995, 638 pages (Bates Numbers L08722-L09359).

EXAMINER LARRY D. DONAGHUE PRIMARY EXAMINER	DATE CONSIDERED 03/24/01

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			FORM PTO-1449		ATTY. DOCKET NO. 0056.10US		APPLICATION NO. 09/223,046

FORM PTO-1449

FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
0056.10US

APPLICATION NO.
09/223,046

APPLICANT
Hsu *et al.*

FILING DATE
December 30, 1998

GROUP
~~2703~~ 2154

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE

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	AP	11	*DSP56002/DSP56L002 Motorola Semiconductor Product Information*, Motorola, Inc., 1994, 3 pages (Bates Numbers L07913-L07915).
	AQ	11	*TMS320C1x/C2x/C2xx/C5x Assembly Language Tools User's Guide*, Texas Instruments, March 1995, 483 pages (Bates Numbers L07916-L08398).
	AR	11	*TMS320C5x General-Purpose Applications User's Guide*, Texas Instruments, July 1997, 167 pages (Bates Numbers L08399-L08565).
	AS		

EXAMINER LARRY D. DONAGHUE PRIMARY EXAMINER

DATE CONSIDERED 02/24/01

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SKGF Rev. 1/95

FORM PTO-1449	ATTY. DOCKET NO. 0056.10US	APPLICATION NO. 09/223,046
FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	APPLICANT Hsu *et al.*	
	FILING DATE December 30, 1998	GROUP ~~2783~~ *2154*

U.S. PATENT DOCUMENTS

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION

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(initialed)	AP	10	Peleg, A. and Weiser, U., "MMX Technology Extension To The Intel Architecture," *IEEE Micro*, IEEE, August, 1996, pp. 42-50, (Bates Numbers L07222-L07230).
(initialed)	AQ	10	El-Sharkawy, Mohamed, Ph.D., *Digital Signal Processing Applications with Motorola's DSP56002 Processor*, Prentice Hall PTR, 1996, pp. 43-124 (Bates Numbers L06519-L06601).
(initialed)	AR	10	*DSP56002 24-Bit Digital Signal Processor User's Manual*, Motorola, Inc., 1995, 386 pages (Bates Numbers L07277-L07662).
(initialed)	AS	10	*DSP56002 Semiconductor Technical Data*, Motorola, Inc., 1996, 110 pages (Bates Numbers L07803-L07912).

EXAMINER LARRY D. DONAGHUE PRIMARY EXAMINER	DATE CONSIDERED 05/24/01

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 0056.10US		APPLICATION NO. 09/223,046
	APPLICANTS Hsu *et al.*		
	FILING DATE December 30, 1998	GROUP ~~2703~~	2 1 ~~4~~

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
							Yes No
							Yes No
							Yes No
							Yes No
							Yes No

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	AP		
	AQ		
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⋀	AS	<u>11</u>	Diefendorff, K., *"The 88110: A Superscalar Microprocessor with Graphics Support"* (Motorola) Microprocessor Forum, November, 1991, 20 pages, (Bates Numbers L00622-00641).

LARRY D. DONAGHUE
PRIMARY EXAMINER

EXAMINER	DATE CONSIDERED 03 06 01

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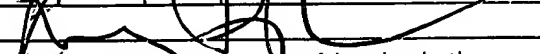
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FORM PTO-1449	ATTY. DOCKET NO. 0056.10US	APPLICATION NO. 09/223,046
INFORMATION DISCLOSURE STATEMENT	APPLICANTS Hsu *et al.*	
	FILING DATE December 30, 1998	GROUP 2785 2 1 5 4

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	AA2						
	AB2						
	AC2						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

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∅	AP	12	Diefendorff, K. and Allen, M., *"Organization of the Motorola 88110: A Superscalar RISC Microprocessor."* Motorola Inc., date unknown, 12 pages, (Bates Numbers L00642-00653).
	AQ		
	AR		
	AS		

EXAMINER	DATE CONSIDERED 07/04/01

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.